

In the Specification:

Kindly amend the specification as follows:

Please replace paragraph [0112] with the following amended paragraph:

When the transmission gate 1211 is ON, the shift ~~resister~~ register functions as a right shift register since a start pulse signal is supplied to the flip-flop 1210 on the most left side. On the other hand, when the transmission gate 1212 is ON, the shift register functions as a left shift register since a start pulse signal is supplied to flip-flop 1210 on the most right side.

Please replace paragraph [0122] with the following amended paragraph:

FIG. 13A shows a configuration of a portable phone as one of the electronic apparatuses of the invention. A module of the portable phone shown in FIG. 13A includes on a printed wiring board 930, a controller 901, a CPU 902, a memory 911, a power supply circuit 903, an audio processing circuit 929, and a transmitter/receiver circuit 904 as well as other elements such as a ~~resister~~ register, buffer, and a capacitor. A light emitting panel 900 is mounted on the printed wiring board 930 through an FPC 908. The light emitting panel 900 includes a pixel portion 905 in which a light emitting element is provided in each pixel, a scan line driver circuit 906 for selecting the pixel in the pixel portion 905, and a signal line driver circuit 907 for supplying a video signal to the selected pixel.

Please replace paragraph [0129] with the following amended paragraph:

The CPU 902 includes a control signal generating circuit 920, a decoder 921, a register 922, an arithmetic circuit 923, a RAM 924, an interface 935 for CPU, and the like. Various signals inputted to the CPU 902 through the interface 935 are once held in the ~~resister~~ register 922, and then inputted to the arithmetic circuit 923, the decoder 921, and the like. The arithmetic circuit 923 performs an arithmetical operation based on the inputted signal, and specifies a location to transfer various commands. Meanwhile, the signal inputted to the decoder 921 is decoded, and then inputted to the control signal generating circuit 920. The control signal generating circuit 920 generates signals including various commands based on the inputted signal, and transfers them to the location specified by the arithmetic circuit 923, specifically such

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Page : 3 of 5

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as the memory 911, the transmitter/receiver circuit 904, the audio processing circuit 929, and the controller 901.